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| (11 AND 26).PGPB,USPT,USOC,EPAB,JPAB,DWPI,TDBD. | 2 |
| (L26 AND L11).PGPB,USPT,USOC,EPAB,JPAB,DWPI,TDBD. | 2 |

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| <u>L34</u> | L26 and l11 | 2 | <u>L34</u> |
| <u>L33</u> | L26 and l10 | 11 | <u>L33</u> |
| <u>L32</u> | L26 and l9 | 1 | <u>L32</u> |
| <u>L31</u> | L26 and l8 | 1 | <u>L31</u> |
| <u>L30</u> | L26 and l7 | 41 | <u>L30</u> |
| <u>L29</u> | L26 and l6 | 96 | <u>L29</u> |
| <u>L28</u> | L26 and l5 | 215 | <u>L28</u> |
| <u>L27</u> | L26 and l22 | 0 | <u>L27</u> |
| <u>L26</u> | bypass\$5 near5 register\$1 and superscalar | 265 | <u>L26</u> |
| <u>L25</u> | bypass\$5 near5 register\$1 and supescalar | 0 | <u>L25</u> |
| <u>L24</u> | bypass45 near5 register\$1 and supescalar | 0 | <u>L24</u> |
| <i>DB=PGPB,USPT; PLUR=YES; OP=OR</i> | | | |

| | | | |
|---|---|-------|------------|
| <u>L23</u> | L22 and l1 | 0 | <u>L23</u> |
| <u>L22</u> | (710/316)[CCLS] | 513 | <u>L22</u> |
| <u>L21</u> | (710/316)![CCLS] | 513 | <u>L21</u> |
| <i>DB=PGPB,USPT,USOC,EPAB,JPAB,DWPI,TDBD; PLUR=YES; OP=OR</i> | | | |
| <u>L20</u> | l2 and superscalar | 62 | <u>L20</u> |
| <u>L19</u> | l3 and superscalar | 18 | <u>L19</u> |
| <u>L18</u> | l4 and superscalar | 33 | <u>L18</u> |
| <u>L17</u> | l2 and L11 | 0 | <u>L17</u> |
| <u>L16</u> | l2 and L10 | 6 | <u>L16</u> |
| <u>L15</u> | l2 and L8 | 1 | <u>L15</u> |
| <u>L14</u> | l2 and L7 | 14 | <u>L14</u> |
| <u>L13</u> | l2 and L6 | 28 | <u>L13</u> |
| <u>L12</u> | l2 and L5 | 76 | <u>L12</u> |
| <i>DB=PGPB,USPT; PLUR=YES; OP=OR</i> | | | |
| <u>L11</u> | (710/305)[CCLS] | 1411 | <u>L11</u> |
| <u>L10</u> | (710/38-317)![CCLS] | 14244 | <u>L10</u> |
| <u>L9</u> | (711/138)[CCLS] | 251 | <u>L9</u> |
| <u>L8</u> | (711/138)[CCLS] | 251 | <u>L8</u> |
| <u>L7</u> | (711/118-221)[CCLS] | 18916 | <u>L7</u> |
| <u>L6</u> | (712/23,215,219, 234, 245)[CCLS] | 1204 | <u>L6</u> |
| <u>L5</u> | (712/2-300)[CCLS] | 10743 | <u>L5</u> |
| <i>DB=PGPB,USPT,USOC,EPAB,JPAB,DWPI,TDBD; PLUR=YES; OP=OR</i> | | | |
| <u>L4</u> | L1 and register\$1 near2 (file\$1 or group\$4) and bypass\$5 | 38 | <u>L4</u> |
| <u>L3</u> | L1 and register\$1 near2 (file\$1 or group\$4) near18 bypass\$5 | 19 | <u>L3</u> |
| <u>L2</u> | L1 and register\$1 near2 (file\$1 or group\$4) | 85 | <u>L2</u> |
| <u>L1</u> | instruction\$1 near5 (plur\$7 or multiple or multi\$7) near5 (fetch\$5) near8 instruction\$1 near3 buffer\$1 | 140 | <u>L1</u> |

END OF SEARCH HISTORY